

Abstract of the Disclosur

A semiconductor integrated circuit includes a unit cell array, a power trunk line, first power branch lines
5 and second power branch lines. The unit cell array includes logic unit cells for providing general circuits and clock unit cells for providing clock drivers. The power trunk line is formed on a peripheral area of the unit cell array. The power trunk line supplies a power
10 source potential. The first power branch lines are formed on the unit cell array for supplying the power source potential to the logic unit cells. The first power branch lines are connected each other and connected to the power trunk line. The second power branch lines are formed on
15 the unit cell array for supplying the power source potential to the clock unit cells. The second power branch lines are connected each other and connected to the power trunk line.